

FIG. 1

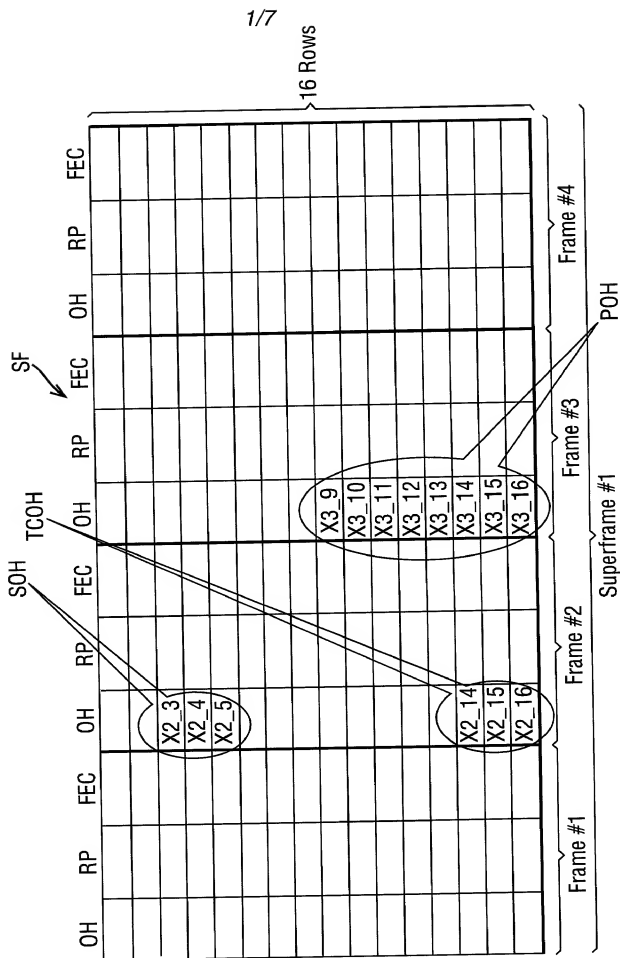


FIG. 2

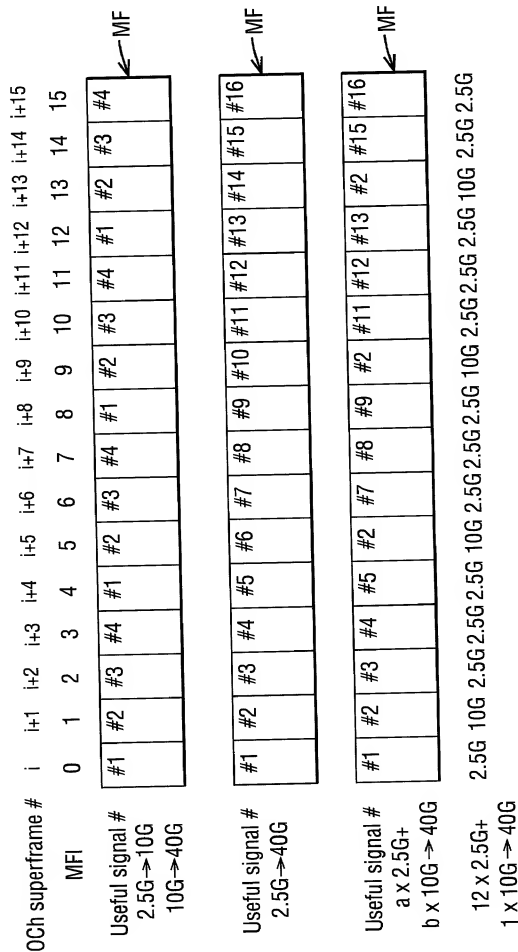


FIG. 3

OH		SPE						Useful signal #
1		2	3	4	5	6		
1	X3_1	OCh-POH bytes with regular meaning	(+)RP	RP	RP	RP	Useful signal #1	
2	X3_2		(+)RP	RP	RP	RP	Useful signal #2	
3	X3_3		(+)RP	RP	RP	RP	Useful signal #3	
4	X3_4		(+)RP	RP	RP	RP	Useful signal #4	
5	X3_5		(+)RP	RP	RP	RP	Useful signal #1	
6	X3_6		(+)RP	RP	RP	RP	Useful signal #2	
7	X3_7		(+)RP	RP	RP	RP	Useful signal #3	
8	X3_8		(+)RP	RP	RP	RP	Useful signal #4	
9	X3_9	Stuff control management information Useful signal #i (i ∈ [1,...,4]) as a function of the MFI 104	(+)RP	RP	RP	RP	Useful signal #1	
10	X3_10		(+)RP	RP	RP	RP	Useful signal #2	
11	X3_11		(+)RP	RP	RP	RP	Useful signal #3	
12	X3_12		(+)RP	RP	RP	RP	Useful signal #4	
13	X3_13	Negative stuff locations (-) (4 bytes) Useful signal #i 105 (i ∈ [1,...,4]) as a function of the MFI	(+)RP	RP	RP	RP	Useful signal #1	
14	X3_14		(+)RP	RP	RP	RP	Useful signal #2	
15	X3_15		(+)RP	RP	RP	RP	Useful signal #3	
16	X3_16		(+)RP	RP	RP	RP	Useful signal #4	

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FIG. 4

OH

SPE

OCh column/row	OH	SPE						Useful signal #
		1	2	3	4	5	6	
1 X3_1	OCh-POH bytes with regular meaning	(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #1
2 X3_2		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #2
3 X3_3		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #3
4 X3_4		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #4
5 X3_5		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #5
6 X3_6		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #6
7 X3_7		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #7
8 X3_8		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #8
9 X3_9	Stuff control management information Useful signal # $(i \in [1, \dots, 16])$ as a function of the MFI 104	(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #9
10 X3_10		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #10
11 X3_11		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #11
12 X3_12		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #12
13 X3_13	Negative stuff locations (-) (4 bytes) Useful signal # 105 $(i \in [1, \dots, 16])$ as a function of the MFI	(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #13
14 X3_14		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #14
15 X3_15		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #15
16 X3_16		(+)RP	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #16

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FIG. 5

OH

SPE

OCh column/row

		SPE						Useful signal #
		1	2	3	4	5	6	
1	X3_1	OCh-POH bytes with regular meaning	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #1
2	X3_2		(+)RP	RP	RP	RP	RP	Useful signal #2
3	X3_3		(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #3
4	X3_4		(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #4
5	X3_5		(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #5
6	X3_6		(+)RP	RP	RP	RP	RP	Useful signal #6
7	X3_7		(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #7
8	X3_8		(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #8
9	X3_9	Stuff control management information Useful signal #i (i ∈ [1, ..., 16]) as a function of the MFI 104	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #9
10	X3_10		(+)RP	RP	RP	RP	RP	Useful signal #10
11	X3_11		(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #11
12	X3_12		(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #12
13	X3_13	Negative stuff locations (-) (4 bytes) Useful signal #i 105 (i ∈ [1, ..., 16]) as a function of the MFI	(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #13
14	X3_14		(+)RP	RP	RP	RP	RP	Useful signal #14
15	X3_15		(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #15
16	X3_16		(+)RP	(+)RP	(+)RP	(+)RP	RP	Useful signal #16

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FIG. 6

Bit #	7	6	5	4	3	2	1	0
	MFI CRA	CRA EDC	EDC	SAI	RAE			
Byte # X3_9	M ₃	C ₁	E ₃	S ₃	R ₁₅	R ₁₁	R ₇	R ₃
Byte # X3_10	M ₂	C ₀	E ₂	S ₂	R ₁₄	R ₁₀	R ₆	R ₂
Byte # X3_11	M ₁	E ₅	E ₁	S ₁	R ₁₃	R ₉	R ₅	R ₁
Byte # X3_12	M ₀	E ₄	E ₀	S ₀	R ₁₂	R ₈	R ₄	R ₀

FIG. 7

Position	5	4	3	2	1	0
Meaning	MFI		CRA	Protective information bits		
HC	M ₃	M ₁	C ₀	E ₄	E ₃	E ₁

FIG. 8

Position	5	4	3	2	1	0
Meaning	MFI		CRA	Protective information bits		
HC	M ₂	M ₀	C ₁	E ₅	E ₂	E ₀

FIG. 9

Bit #	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Meaning	Parity		Rate matching extension RAE													
	P ₁	P ₀	R ₁₃	R ₁₂	R ₁₁	R ₁₀	R ₉	R ₈	R ₇	R ₆	R ₅	R ₄	R ₃	R ₂	R ₁	R ₀